

S/N 08/650,719

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Jeffrey S. Mailloux et al.

Examiner: Hong Kim

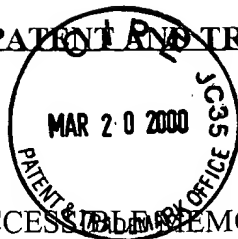
Serial No.: 08/650,719

Group Art Unit: 2751

Filed: May 20, 1996

Docket: 303.623US1

Title: ASYNCHRONOUSLY-ACCESSIBLE MEMORY DEVICE WITH MODE
SELECTION CIRCUITRY FOR BURST OR PIPELINED OPERATION



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AMENDMENT AND RESPONSE

Assistant Commissioner for Patents
Washington, D.C. 20231

Applicant has reviewed the Office Action mailed on December 15, 1999. Please amend the above-identified patent application as follows.

IN THE CLAIMS

Please amend the claims as follows:

46. (Amended) A method for accessing several different locations in an asynchronously-accessible memory device, comprising:

- selecting a [burst] pipelined mode of operation;
- providing a new external address for every access associated with asynchronously accessing the asynchronously-accessible memory device while in the [burst] pipelined mode of operation;
- switching modes to a [pipelined] burst mode of operation;
- providing an initial external address associated with asynchronously accessing the asynchronously-accessible memory device in the [pipelined] burst mode of operation; and
- generating at least one subsequent internal address patterned after the initial external address while in the [pipelined] burst mode of operation.

47. (Amended) A method for accessing several different locations in an asynchronously-accessible memory device, comprising:

- selecting a pipeline mode of operation;